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| 710                  | 240      |
| Class                | Subclass |
| ISSUE CLASSIFICATION |          |

PATENT NUMBER

## U.S. UTILITY Patent Application

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| Q.I.P.E. <i>R.H.</i><br>(2)<br>SCANNED <i>SB</i> Q.A. <i>W</i> | PATENT DATE<br>. |
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| APPLICATION NO.<br>09/637846 | CONT/PRIOR | CLASS<br>700 710 | SUBCLASS<br>240 | ART UNIT<br>2121<br>2409 | EXAMINER<br>HUYANH |
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## APPLICANTS

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Appanagari Raghavendra

# TITLE

Peripheral component interconnect arbiter implementation with dynamic priority scheme

PTO-2040  
12/99

| <b>ISSUING CLASSIFICATION</b>       |                 |                           |  |  |  |  |  |  |
|-------------------------------------|-----------------|---------------------------|--|--|--|--|--|--|
| <b>ORIGINAL</b>                     |                 | <b>CROSS REFERENCE(S)</b> |  |  |  |  |  |  |
| <b>CLASS</b>                        | <b>SUBCLASS</b> | <b>CLASS</b>              | <b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b> |  |  |  |  |  |
| 710                                 | 240             | 710                       | 244                                      |  |  |  |  |  |
| <b>INTERNATIONAL CLASSIFICATION</b> |                 |                           |  |  |  |  |  |  |
| G O / G F                           | 13138           |                           |  |  |  |  |  |  |
|                                     |                 |                           |  |  |  |  |  |  |
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| <input type="checkbox"/> <b>TERMINAL<br/>DISCLAIMER</b>  | <b>DRAWINGS</b>                                 |                             |                            | <b>CLAIMS ALLOWED</b>                                    |                                      |
|  | Sheets Drwg.<br><div>5</div>                    | Figs. Drwg.<br><div>5</div> | Print Fig.<br><div>3</div> | Total Claims<br><div>9</div>                             | Print Claim for O.G.<br><div>1</div> |
| The term of this patent<br>subsequent to _____ (date)<br>has been disclaimed.  | <div>Kim T. Huynh</div><br>(Assistant Examiner) |                             |                            | <b>NOTICE OF ALLOWANCE MAILED</b><br><br>_____<br>_____  |                                      |
| The term of this patent shall<br>not extend beyond the expiration date<br>of U.S Patent. No. _____<br><br>_____<br><br>_____   | _____<br>(Primary Examiner)                     |                             |                            | <b>ISSUE FEE</b><br><br>Amount Due _____ Date Paid _____ |                                      |
| The terminal ____ months of<br>this patent have been disclaimed.   | _____<br>(Legal Instruments Examiner)           |                             |                            | <b>ISSUE BATCH NUMBER</b><br><b>COPY</b>                 |                                      |
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